



50277-1763

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Confirmation No.: 2140

Sashikanth Chandrasekaran, et al.

Group Art Unit No.: 2161

Serial No.: 10/056,716

Examiner: CHEN, TE Y

Filed: January 22, 2002

For: SEMANTIC RESPONSE TO LOCK  
REQUESTS TO REDUCE COHERENCE  
OVERHEAD IN MULTI-NODE SYSTEMS

Mail Stop RCE  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE**

Sir:

This is in response to the Office Action mailed March 29, 2005, the shortened statutory period for which runs until June 29, 2005. Accompanying this response is a replacement sheet for FIG. 3.